

FIG. 1

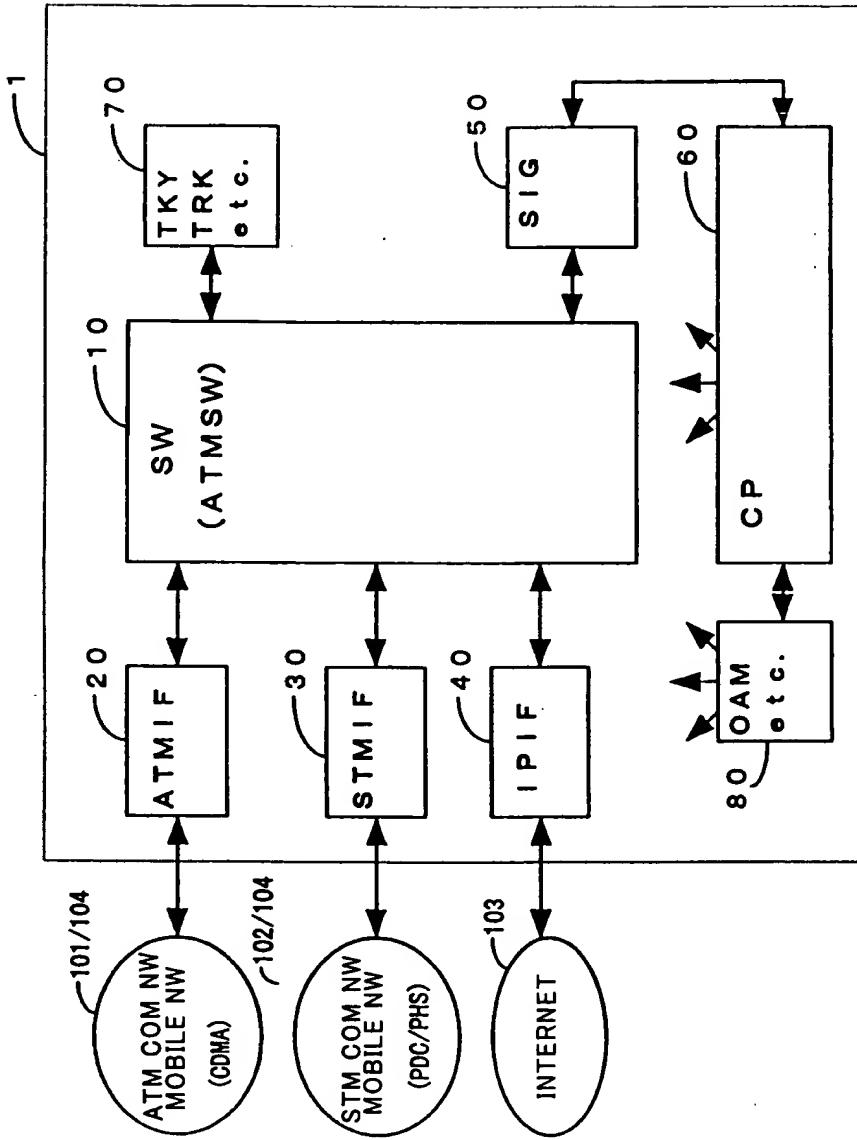


FIG. 2

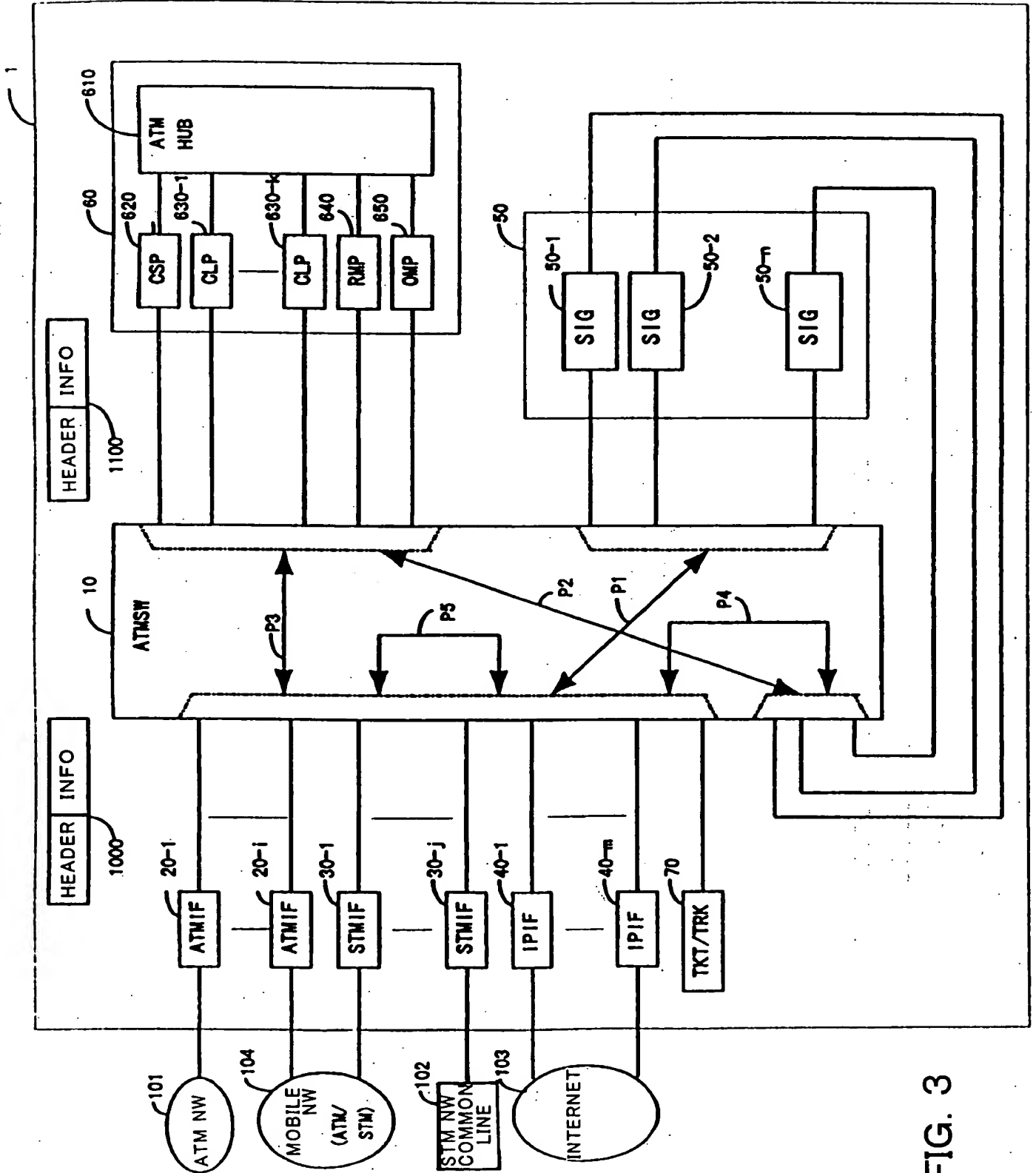


FIG. 3

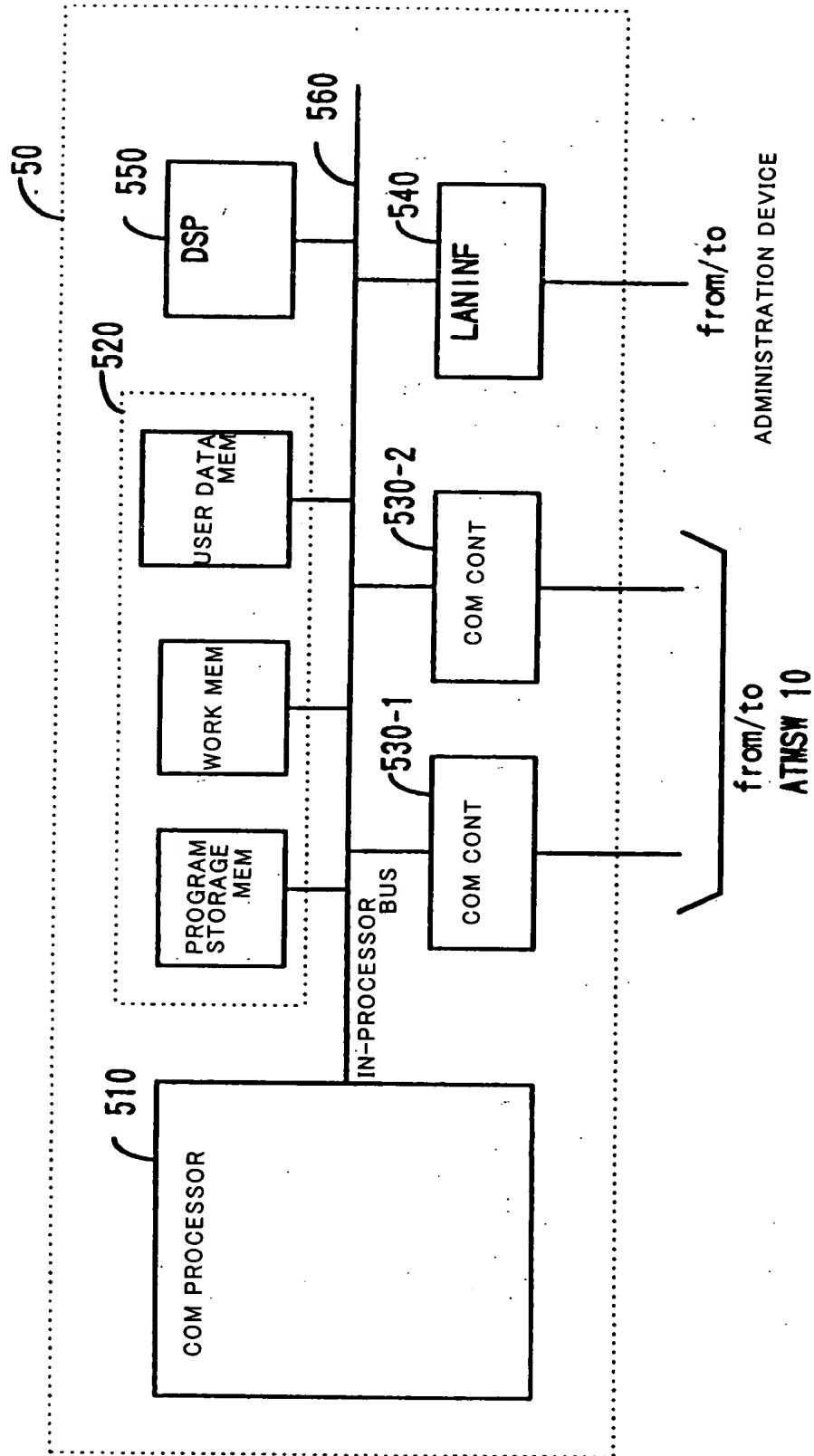


FIG. 4

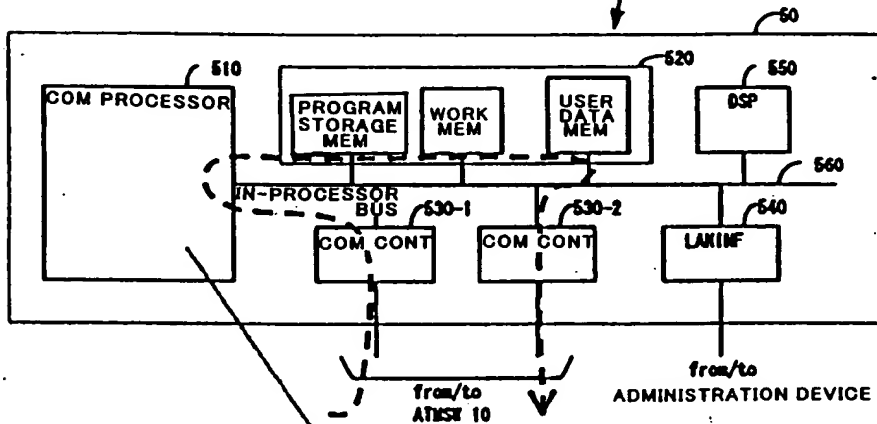
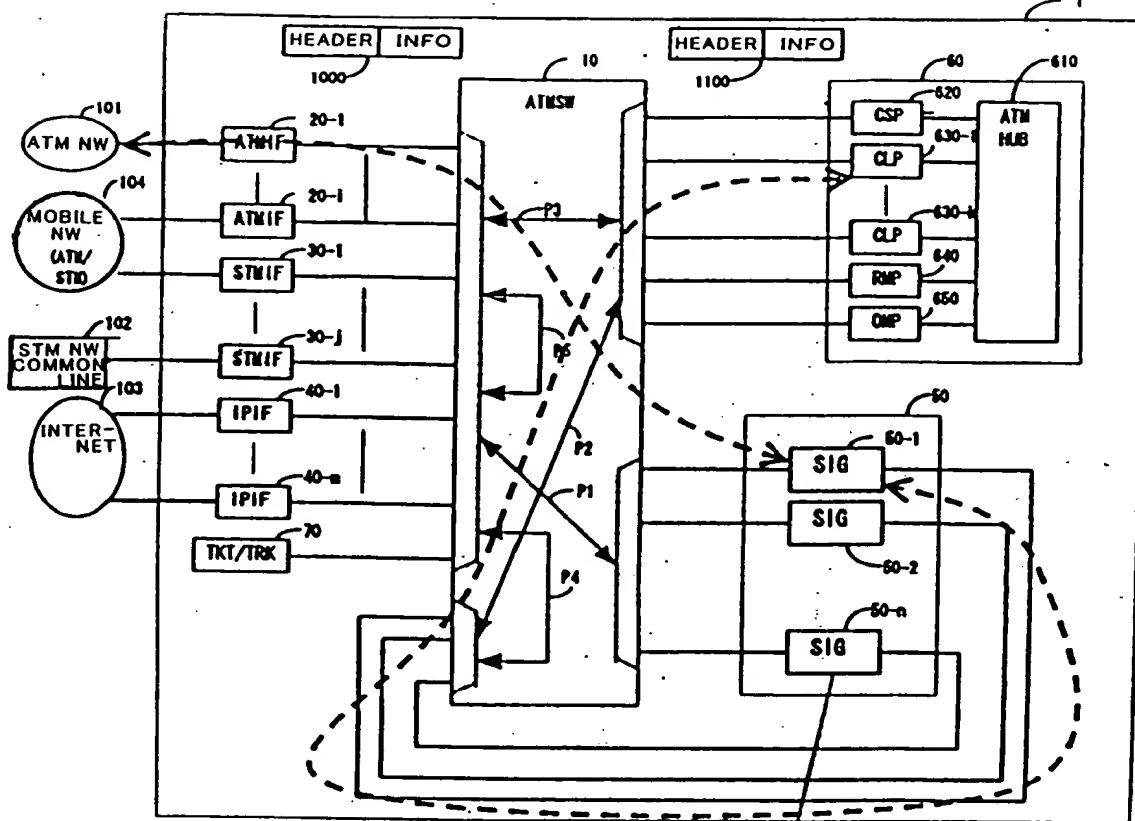
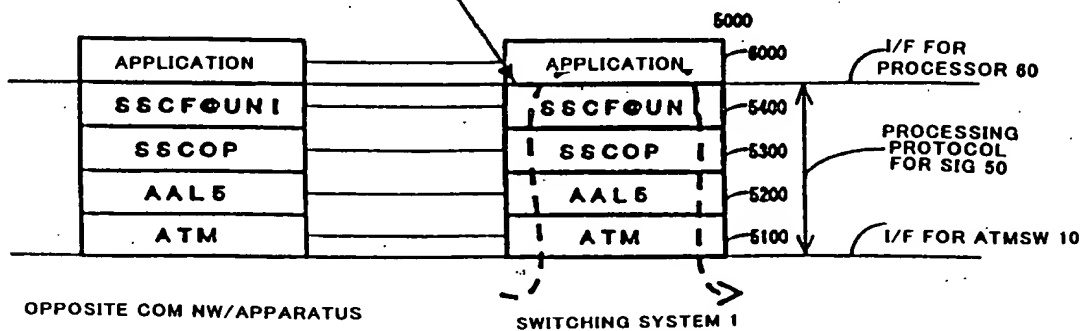
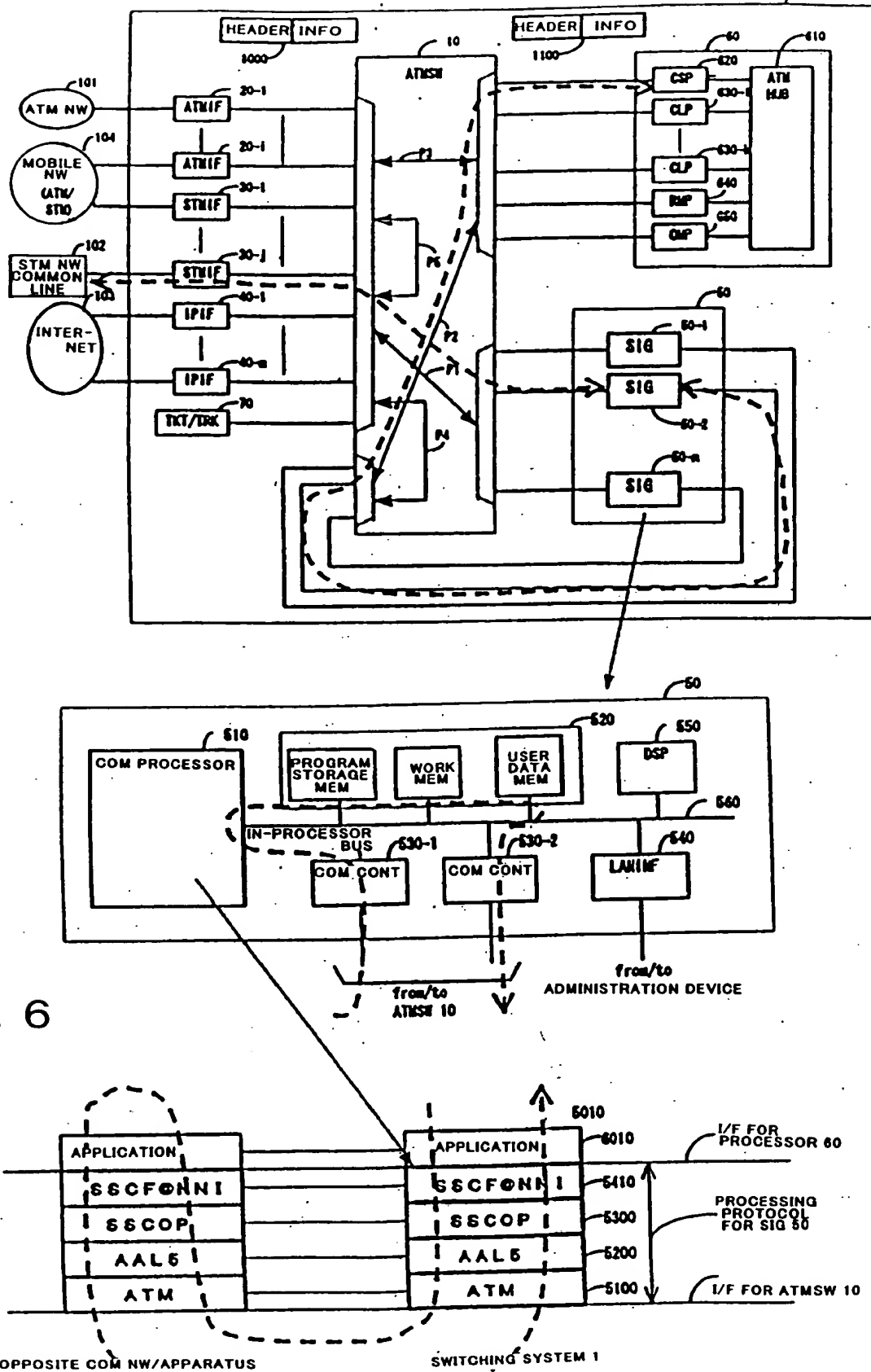


FIG. 5





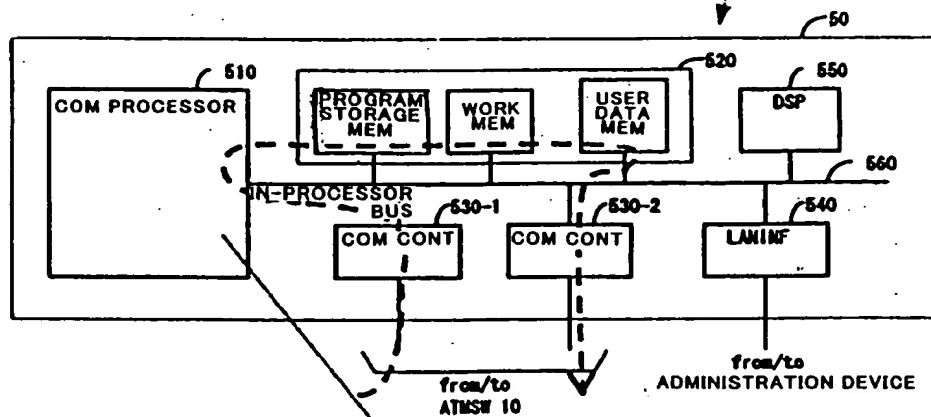


Diagram illustrating the processing protocol for SIG 50, showing the protocol stack structure and associated addresses.

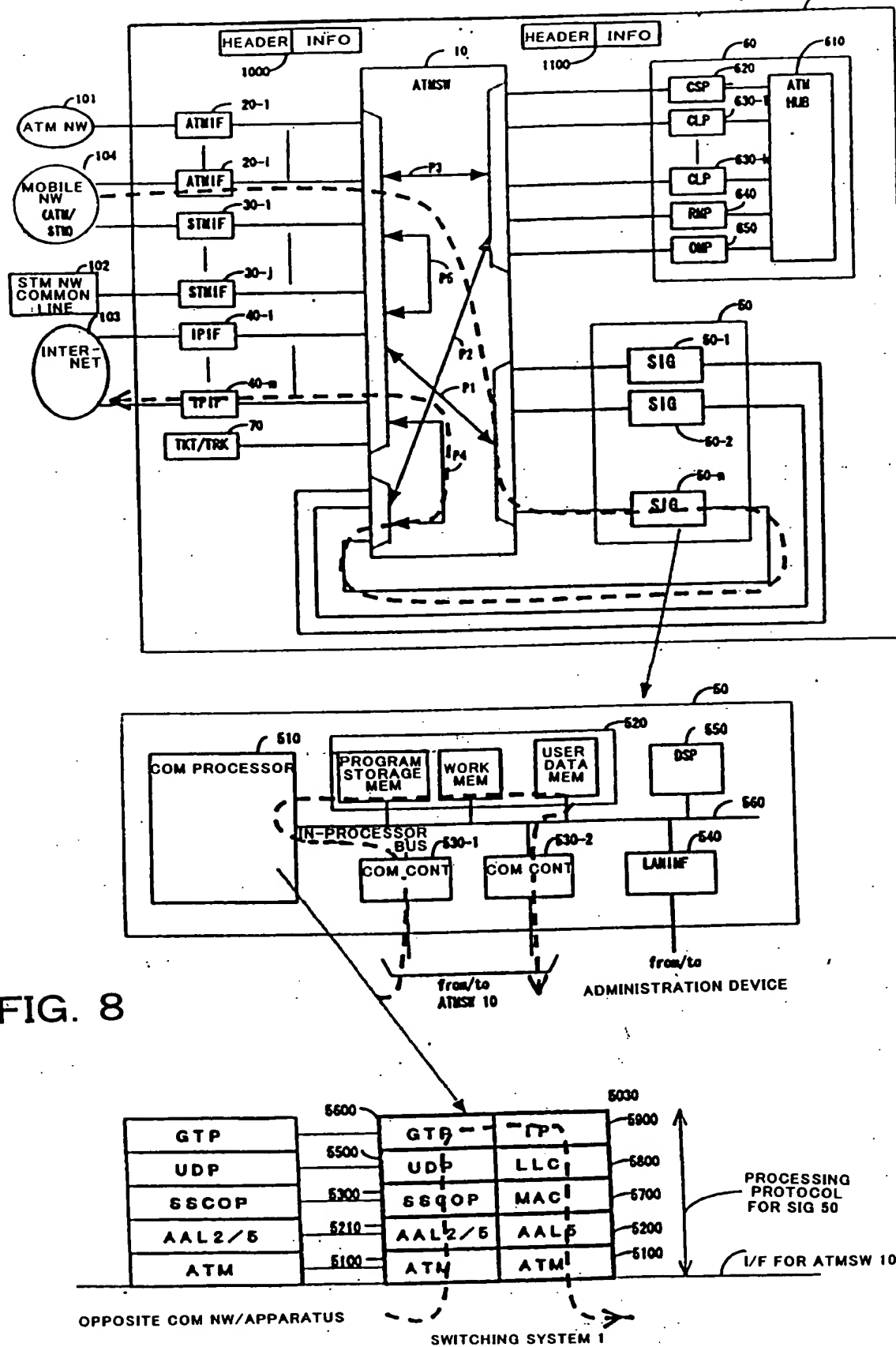
The diagram shows two protocol stacks:

- OPPOSITE COM NW/APPARATUS:** Consists of layers GTP, UDP, SSCOP, AAL2/5, and ATM.
- SWITCHING SYSTEM 1:** Consists of layers GTP, UDP, SSCOP, AAL2/5, and ATM.

Addresses associated with the layers in the SWITCHING SYSTEM 1 stack:

- 5020 (Top of the stack)
- 5600 (Between GTP and UDP)
- 5500 (Between UDP and SSCOP)
- 5300 (Between SSCOP and AAL2/5)
- 5210 (Between AAL2/5 and ATM)
- 5100 (Bottom of the stack)

A vertical double-headed arrow indicates the **PROCESSING PROTOCOL FOR SIG 50** spanning the layers. A curved arrow at the bottom right indicates the **I/F FOR ATMSW 10**.





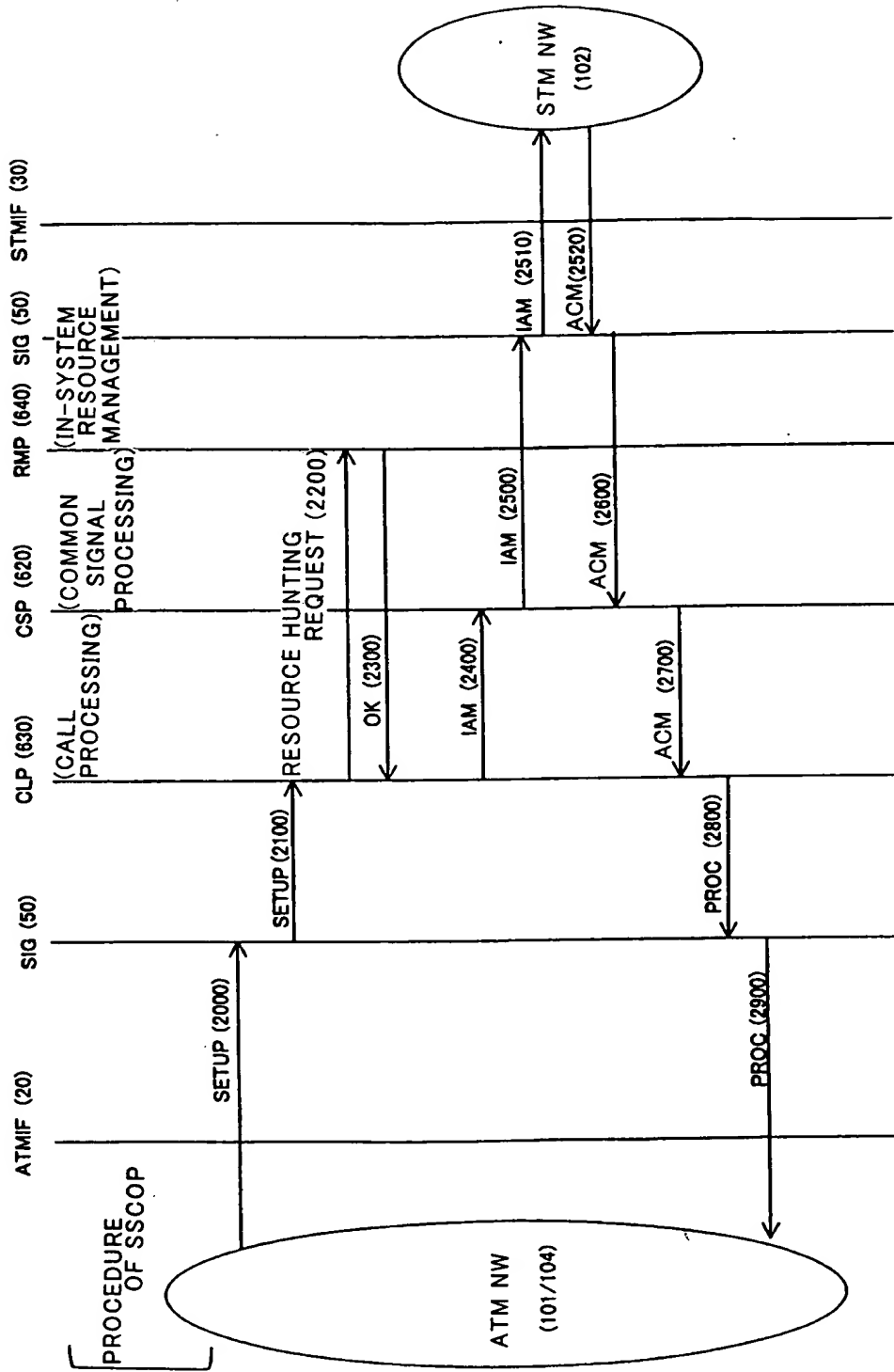


FIG. 9